

# MCS 202 SYLLABUS

## **Block-1 Data Representation and Logic Circuit**

Unit-1 A Computer System

Unit-2 Data Representation

Unit-3 Logic circuits: an introduction

Unit-4 Logic circuits: sequential circuits

## **Block-2 Memory and Input / Output Organisation**

Unit-5 The Memory system

Unit-6 Advance memory organisation

Unit-7 Input/output Organisation

Unit-8 Device Technology

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## **Block-3 The Processing Unit**

Unit-9 Instruction Set Architecture

Unit-10 Registers, Micro-operations and Instruction Execution

Unit-11 The Control Unit

Unit-12 Reduced Instruction Set Computer Architecture

## **Block-4 Microprocessor and Advanced Architectures**

Unit-13 Microprocessor Architecture

Unit-14 Introduction to Assembly Language Programming

Unit-15 Assembly Language Programming

Unit-16 Advanced Architectures

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